

17. (NEW) A flat plasma display as claimed in claim 15, wherein said drive control signal control unit controls an operation of a display panel driving unit by changing said drive control signals in response to said detected specific signal.

18. (NEW) A flat plasma display as claimed in claim 15, wherein said control signal control unit and said internal power supply controlling unit stop operating if said specific signal is at a first level and start operating if said detected specific signal is at a second level, and thereby the drive control signals are controlled in response to a level of said detected specific signal.

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19. (NEW) A flat plasma display comprising a three-electrode surface discharge AC plasma display, further comprising:
an external signal detection unit detecting a specific signal input to said flat plasma display from an external source; and
a drive control signal control unit controlling drive control signals of said flat plasma display in response to said detected specific signal.

20. (NEW) A flat plasma display as claimed in claim 19, wherein said three-electrode surface discharge AC plasma display further comprises:
first and second electrodes arranged in parallel with each other; and
third electrodes orthogonal to said first and second electrodes, said first electrodes being commonly connected together and said second electrodes being arranged to define respective display lines, wherein said flat plasma display has a surface discharge structure employing wall charges as a memory.

21. (NEW) A flat plasma display as claimed in claim 20, wherein said three-electrode surface discharge AC plasma display further comprises:
a first substrate, said first and second electrodes being arranged in parallel to each other on said first substrate and paired for defining respective display lines;
a second substrate spaced apart from and facing said first substrate, defining a cavity therebetween, said third electrodes being arranged on said second substrate in orthogonal relationship to said first and second electrodes and displaced therefrom;
wall charge accumulating dielectric layers respectively covering the surfaces of said first and second electrodes;